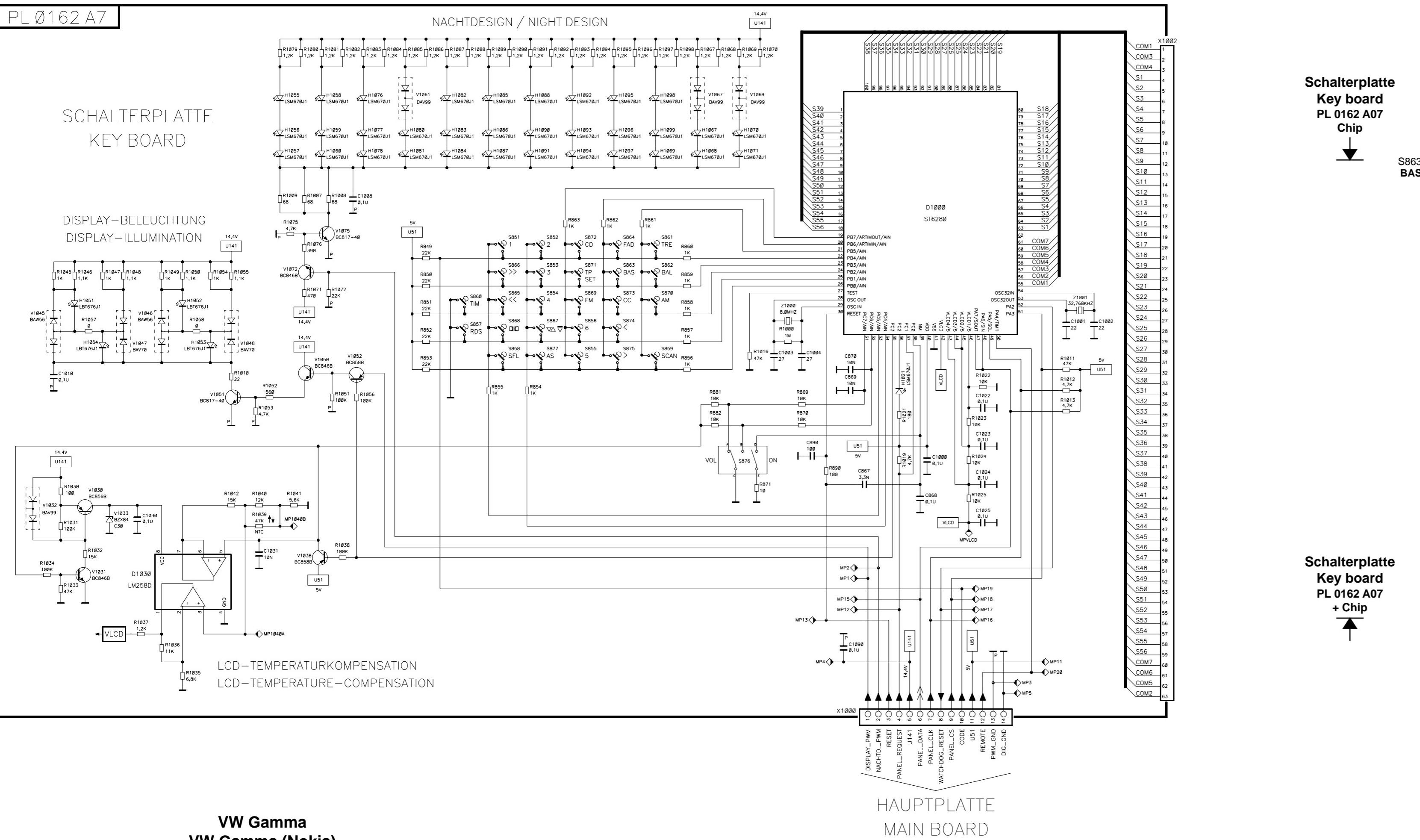
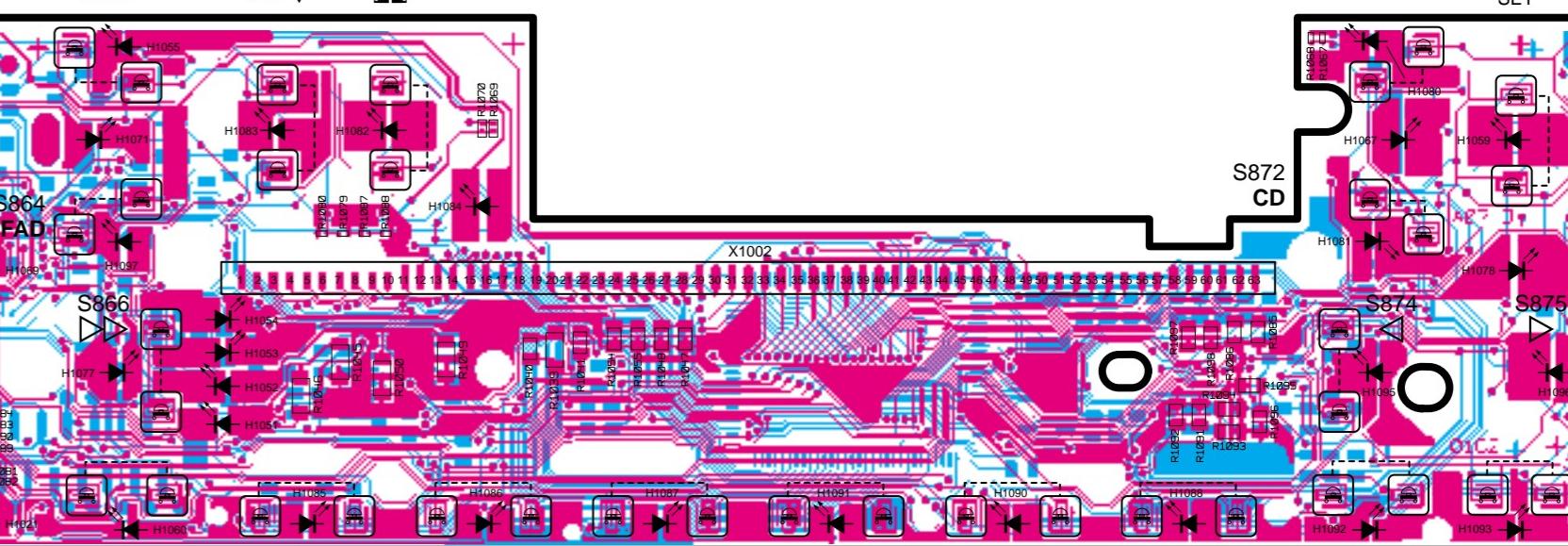
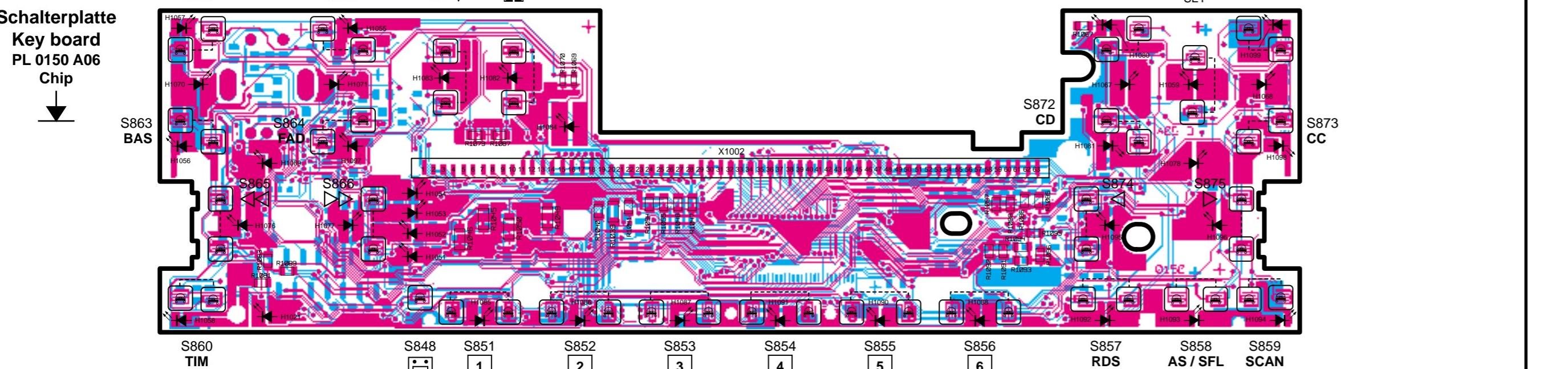


Pin-Belegung des IC D150 Digital IC D150 Pin Configuration				
Pin No.	I/O	Name	Funktion	Function
1	-	VSS	Masse	Ground
2	-	VDD	5 V	5 V
4	I	TDI	Testdateneingang	Test Data Input
5	I	TMS	Test Mode	Testmode
6	I	TCKL	Test Clock	Testclock
7	-	VDD	5 V	5 V
8	-	VSS	Masse	Ground
9	-	PWDN	Power down Zustand	Power down Mode
10	I	RESN	Reset	Hardware reset (active LOW)
11	O	IRQN	RDS Alarm/SLS	RDS alarm/search stop
12	I	CSN	Chip select Eingang	Chip select µC interface
13	I	SCPRX	Serielle Daten µC Interface	Serial data µC interface IN
14	O	SCPTX	Serielle Daten µC Interface	Serial data µC interface OUT
15	I	SCPCK	Clock µC Interface	Clock µC interface
16	-	VDDIO	Plusspannung Digitale Ein-/Ausgänge	Voltage for digital I/O
17	-	VSSIO	Masse Digitale Ein-/Ausgänge	Ground for digital I/O
18	O	CLK1	Programmierbarer Clock 1	Programmable clock 1
20	I	XTAL1	28,5 MHz Oszillator	Oscillator 28,5 MHz
21	O	XTAL0	28,5 MHz Oszillator	Oscillator 28,5 MHz
31	I	TDI1	Testdateneingang 1	Test Input 1
32	-	VDD	5 V	5 V
33	-	VSS	Masse	Ground
35	-	VSSPLL	Masse (Minus) PLL	Ground (minus) PLL
36	-	VDDPLL	Plus PLL 5V	PLL 5V (pos.)
37	O	REFP1	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
38	O	REFN1	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
39	-	CAPN	PLL Kapazität (negativ)	PLL capacity (neg.)
40	-	CAPP	PLL Kapazität (positiv)	PLL capacity (pos.)
41	-	VDDO	Audio D/A - Wandler 5V	Audio D/A converter (+5V)
42	-	VSSO	Audio D/A - Wandler Masse	Audio D/A converter (ground)
44	O	RFO	Audio Rechts (analog)	Analogic audio right
45	-	OGND	Masse Analogausgänge	Ground
46	-	LFO	Audio Links (analog)	Analogic audio left
48	-	VDDA	5V A/D - Wandler	5V A/D - converter
49	-	VSSA	Masse A/D - Wandler	Ground A/D - converter
50	O	REFP3	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
51	O	REFN3	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
52	I	AUXL	Externer Eingang links	Auxillary left
53	I	CCL	Cassette Eingang links	Cassette input left
54	-	AGND	Audioeingänge Masse	Ground for Audio inputs
55	I	CCR	Cassette Eingang rechts	Cassette input right
56	I	AUXR	Externer Eingang rechts	Auxillary left right
57	-	VDDR	5 V	5 V
58	-	VSSR	Masse	Ground
59	O	REFP2	Audio D/A-Wandler Positive Referenz	Audio D/A converter (pos. reference)
60	I	IFP	ZF Eingang (plus)	Positif IF input
61	I	IFN	ZF Eingang (minus)	IF input (neg.)
62	O	REFN2	Audio D/A-Wandler Negative Referenz	Audio D/A converter (neg. reference)
63	-	VSSIF	ZF A/D - Wandler (minus)	IF A/D converter (-)
64	-	VDDIF	ZF A/D - Wandler 5 V	IF A/D converter (+5V)

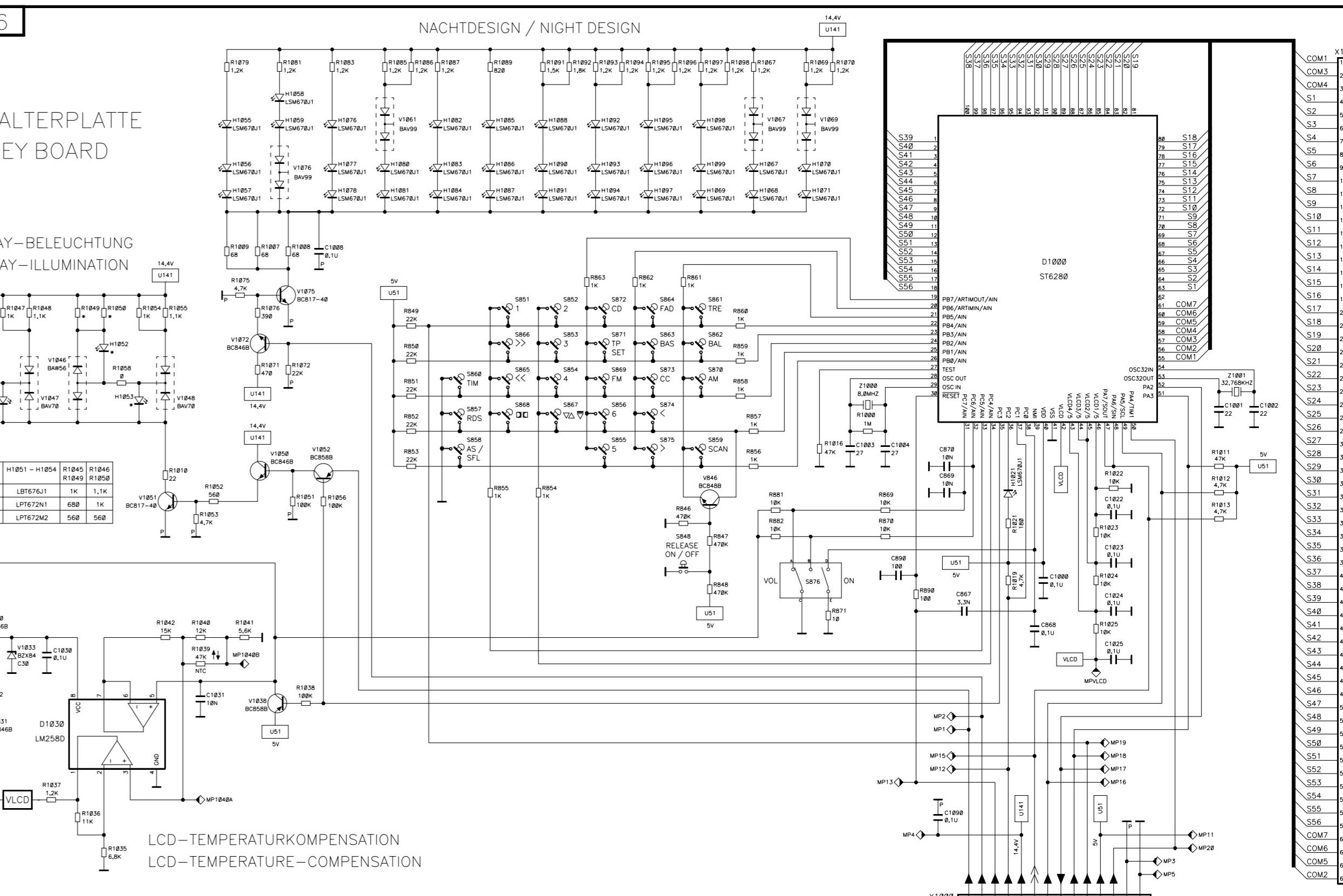
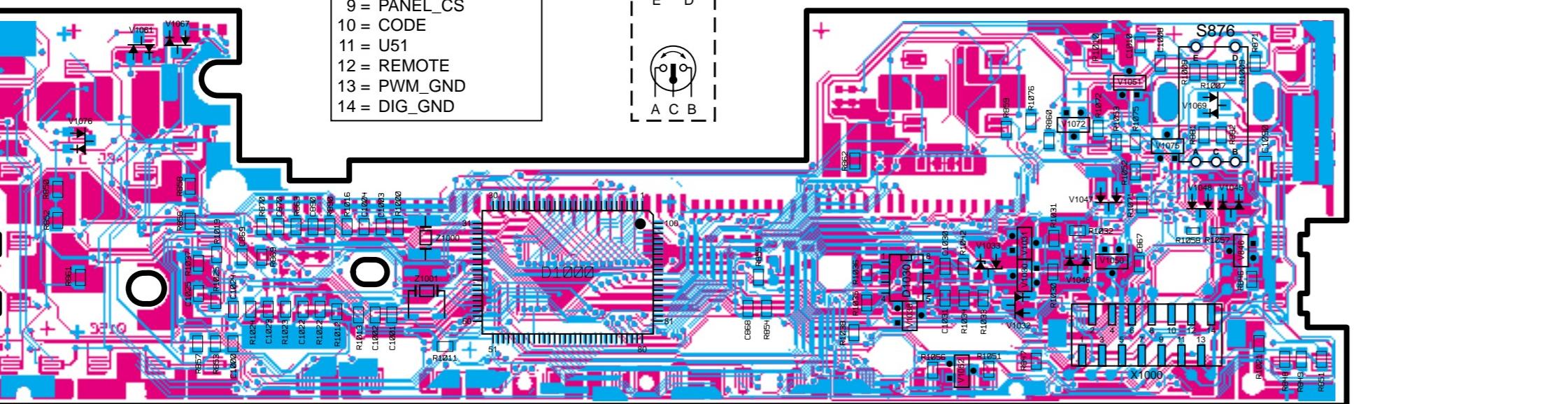


**Schalterplatte
Key board
PL 0162 A7
Chip**





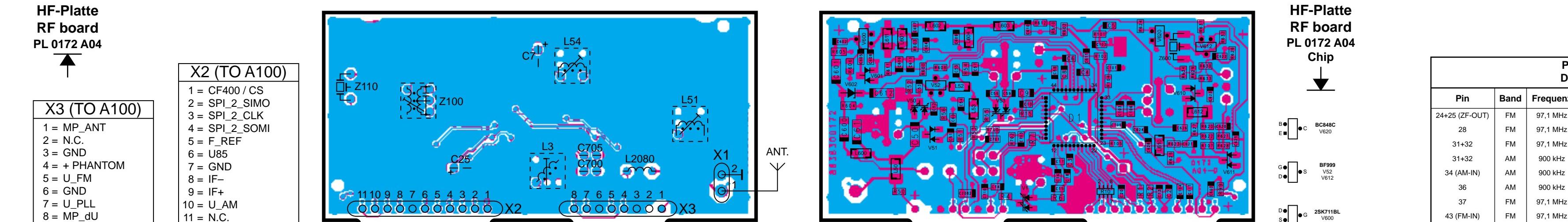
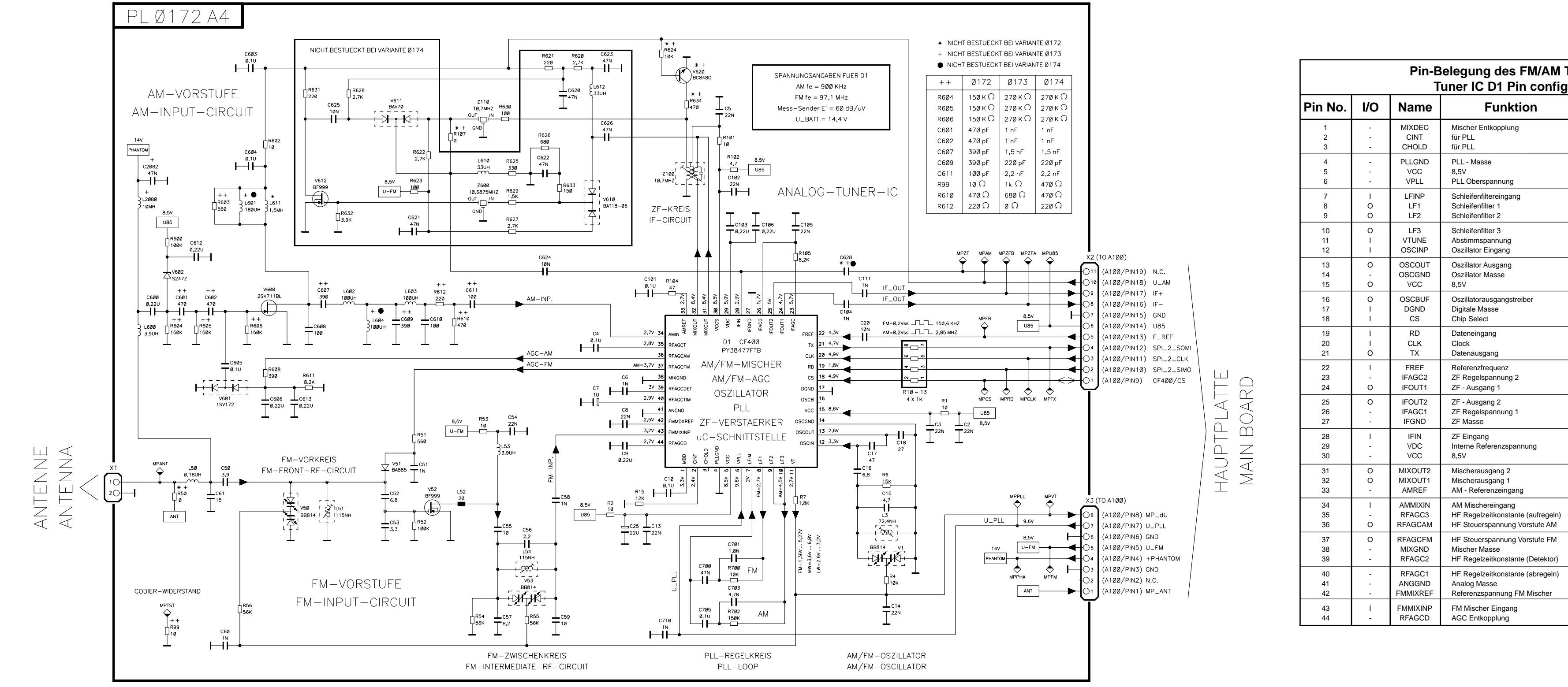
Schalterplatte Key board PI_0150_A06



Gamma Release

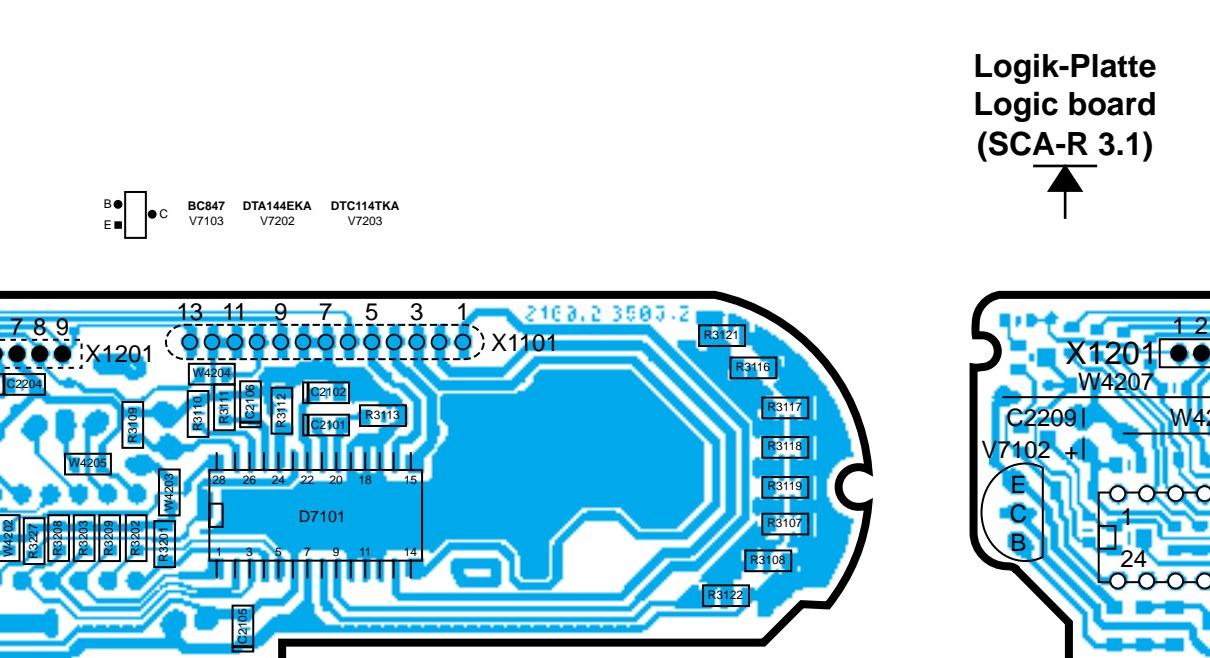
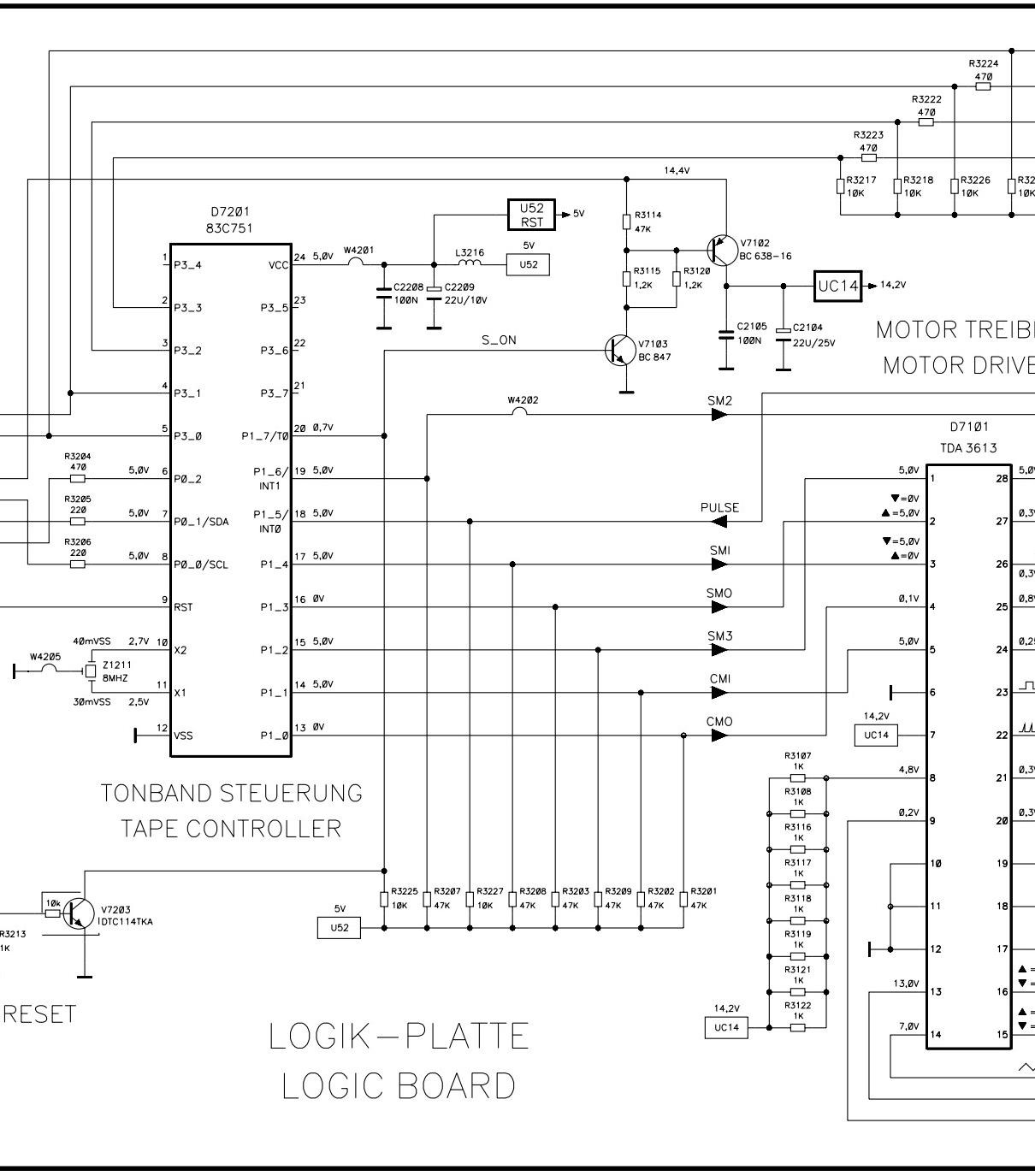
a Release (Nokia)

Gamma Release



Tuner-IC D1 Configuration	
	Function
	Mixer decoupling for PLL for PLL
	PLL Ground 8,5V PLL top voltage
	PLL loop filter Input PLL loop filter Output 1 PLL loop filter Output 2
	PLL loop filter Output 3 Tuning voltage Oscillator Input
	Oscillator Output Oscillator Ground 8,5V
	Oscillator Buffer Output Digital Ground Chip Select
	DATA IN Clock DATA OUT
	Reference frequency IF AGC 2 IF output 1
	IF output 2 IF AGC 1 IF Ground
	IF Input Internal reference voltage 8,5V
	Mixer Output 2 Mixer Output 1 AM reference Input
	AM Mixer Input RF AGC 3 RF AGC for AM input stage
	RF AGC for FM input stage Mixer Ground RF AGC 2
	RF AGC 1 Analog ground Reference voltage FM mixer
	FM mixer input AGC decoupling

SCA-R 3.1



X1101	1 = CAPSTAN_MOTOR - 2 = CAPSTAN_MOTOR + 3 = CAPSTAN_MOTOR 4 = LIFT_MOTOR - 5 = LIFT_MOTOR + 6 = SW_STANDBY 7 = SW_STANDBY_GND	8 = SW_PLAY_GND 9 = SW_PLAY 10 = SW_INSERT 11 = SW_INSERT_GND 12 = SW_ME / CR_GND 13 = SW_ME / CR
-------	---	--

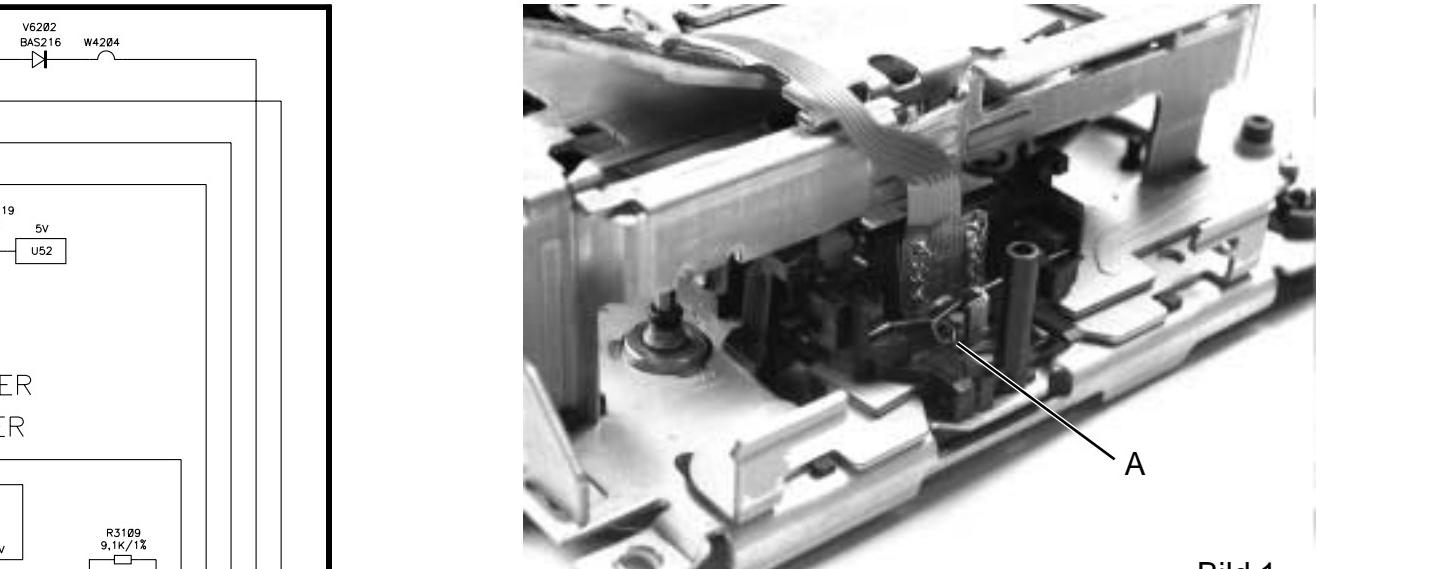


Bild 1 Figure 1